

ABSTRACT OF THE DISCLOSURE

The invention aims to easily synthesize a plurality of image information data by using more various and complicated methods, with the circuit size and the manufacturing costs 5 remaining reduced. A control unit supplies different synthesizing image information data to the first and second plane memories and supplies a superposition control information data to a superposing order control unit. Based on the address information data supplied from the address generation counter, 10 the superposing order control unit refers to the held superposition control information data and controls operation of switch circuits. Based on the control, the first switch circuit reads and outputs the synthesis image information data from the first or second plane memory. The second switch circuit 15 outputs an input image signal or the output from the switch circuit to the outside of the image synthesizing apparatus. The present invention is applicable to a camcorder.